L Number	Hits	Search Text	DB	Time stamp
1	997799	(static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:26
2	68882	write near2 (port or circuit or control$4 or transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:27
3	135092	read near2 (port or circuit or control$4 or transistor)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:27
4	1453	first near (write near2 (port or circuit or control$4 or transistor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:28
5	1135	second near (write near2 (port or circuit or control$4 or transistor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:28
6	2315	first near (read near2 (port or circuit or control$4 or transistor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:28
7	1780	second near (read near2 (port or circuit or control$4 or transistor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:28
8	38850	((static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control$4 or transistor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:29
9	25730	(((static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control$4 or transistor))) and (read near2 (port or circuit or control$4 or transistor))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:29
10	796	((((static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control$4 or transistor))) and (read near2 (port or circuit or control$4 or transistor))) and (first near (write near2 (port or circuit or control$4 or transistor)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:29
11	347	(((((static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control$4 or transistor))) and (read near2 (port or circuit or control$4 or transistor))) and (first near (write near2 (port or circuit or control$4 or transistor)))) and (second near (write near2 (port or circuit or control$4 or transistor)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:29

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12	171	(((((((static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control$4 or transistor))) and (read near2 (port or circuit or control$4 or transistor))) and (first near (write near2 (port or circuit or control$4 or transistor)))) and (second near (write near2 (port or circuit or control$4 or transistor)))) and (first near (read near2 (port or circuit or control$4 or transistor)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:29
13	138	(((((((static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control$4 or transistor))) and (read near2 (port or circuit or control$4 or transistor))) and (first near (write near2 (port or circuit or control$4 or transistor)))) and (second near (write near2 (port or circuit or control$4 or transistor)))) and (first near (read near2 (port or circuit or control$4 or transistor)))) and (second near (read near2 (port or circuit or control$4 or transistor)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:30
14	28	(((((((((static near2 ram) or (static near2 memory) or sram or latch or register or inverter or invertor) and (write near2 (port or circuit or control$4 or transistor))) and (read near2 (port or circuit or control$4 or transistor))) and (first near (write near2 (port or circuit or control$4 or transistor)))) and (second near (write near2 (port or circuit or control$4 or transistor)))) and (first near (read near2 (port or circuit or control$4 or transistor)))) and (second near (read near2 (port or circuit or control$4 or transistor)))) and "nmos transistor"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/03/16 09:31